

IT-B2

SERVICE MANUAL

Mexican Model

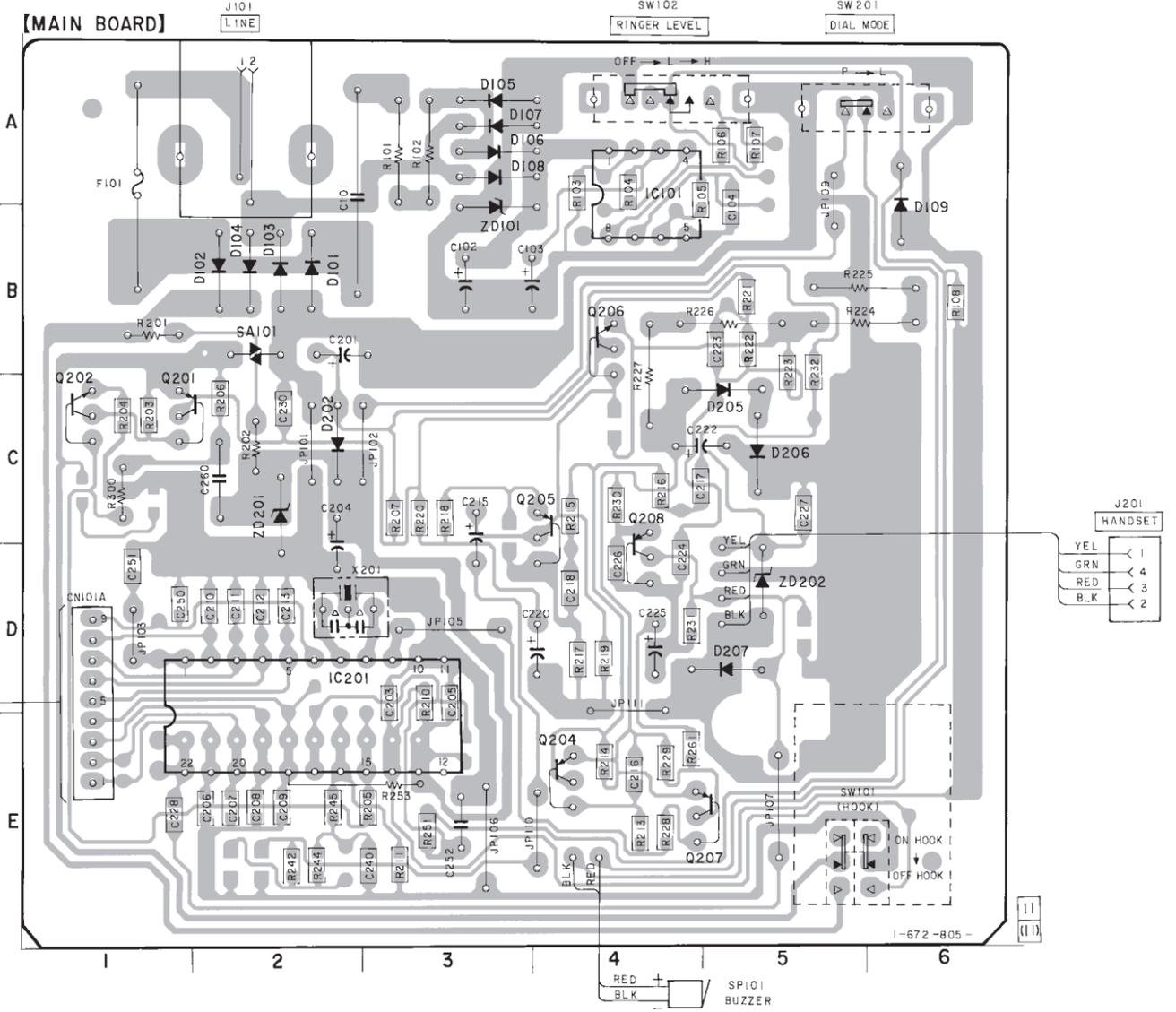
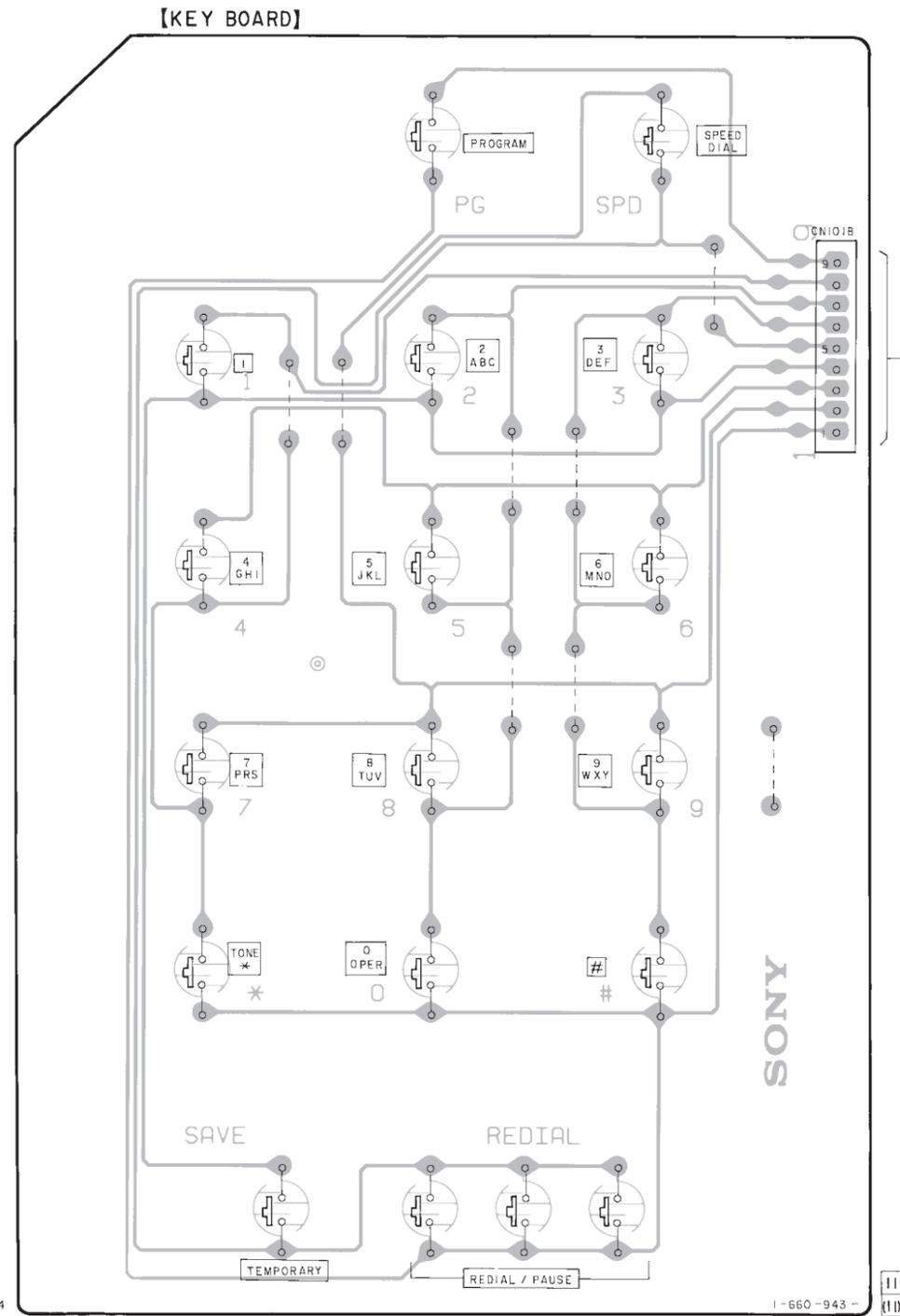


SECTION 3
DIAGRAMS

3-1. PRINTED WIRING BOARDS

• Semiconductor Location

Ref. No.	Location
D101	B-2
D102	B-2
D103	B-2
D104	B-2
D105	A-3
D106	A-3
D107	A-3
D108	A-3
D109	B-6
D202	C-2
D205	C-5
D206	C-5
D207	D-5
IC101	A-4
IC201	D-2
Q201	C-1
Q202	C-1
Q204	E-4
Q205	C-4
Q206	B-4
Q207	E-4
Q208	C-4
ZD101	B-3
ZD201	C-2
ZD202	D-5



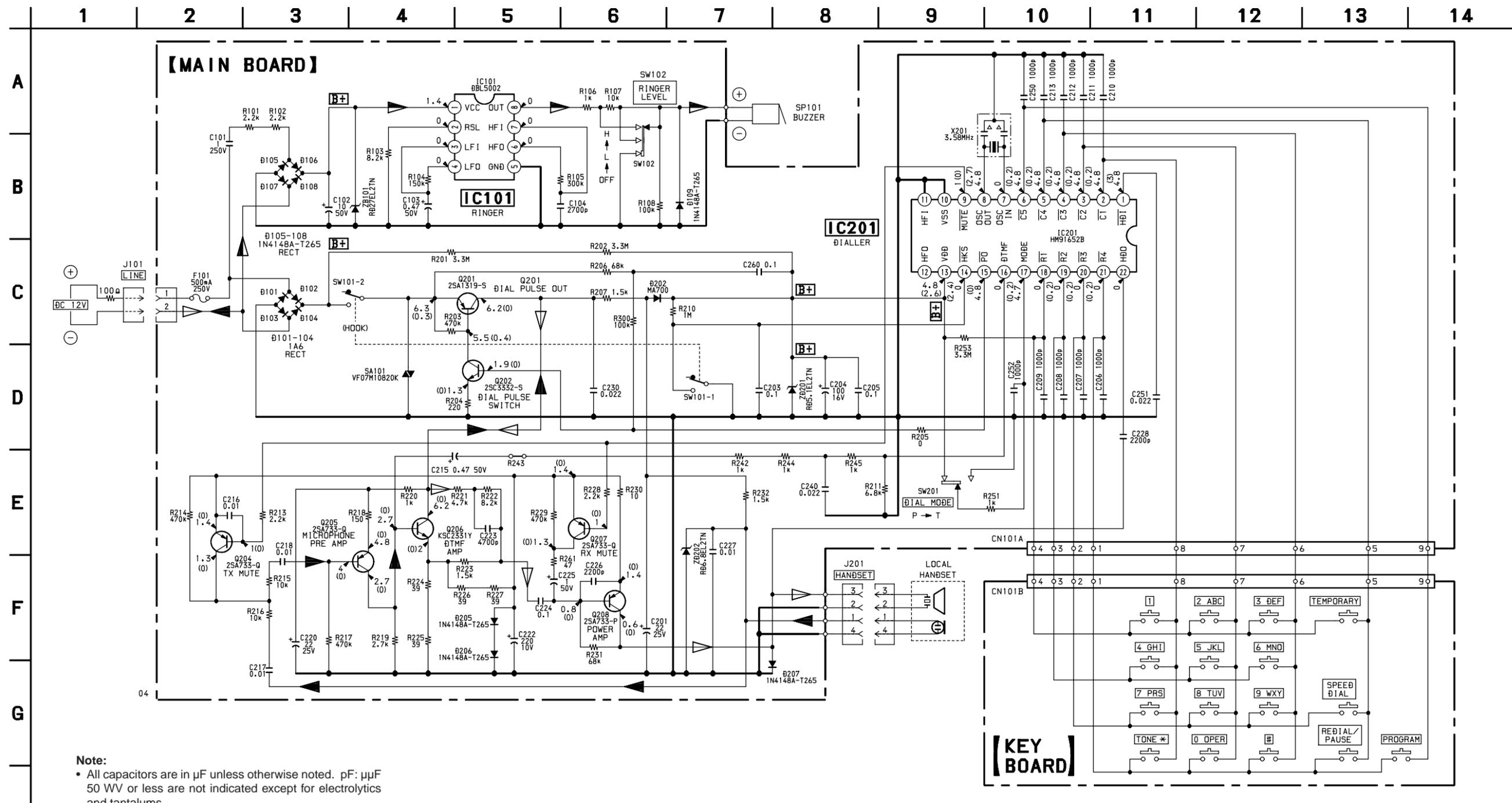
04

I-660-943-

I-672-805-

- Note:**
- : parts extracted from the component side.
 - - - ○ : Carbon pattern.
 - △ : internal component.
 - ▨ : Pattern from the side which enables seeing.

3-2. SCHEMATIC DIAGRAM • Refer to page 6 for IC Block Diagrams.

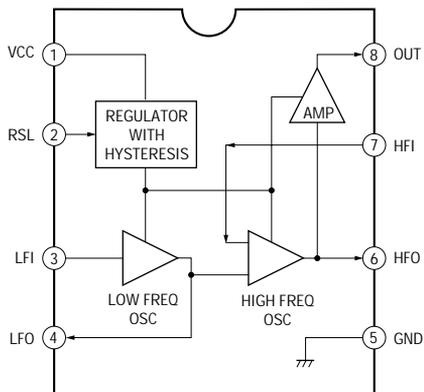


Note:

- All capacitors are in μF unless otherwise noted. pF : μpF 50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and $1/4\text{W}$ or less unless otherwise specified.
- Δ : internal component.
- : panel designation.
- **B+** : B+ Line.
- Power voltage is dc 12V and fed with regulated dc power supply from J101 with 100 Ω in series.
- Voltage is dc with respect to ground under no-signal condition.
- no mark : OFF HOOK
() : ON HOOK
- Voltages are taken with a VOM (Input impedance 10 M Ω). Voltage variations may be noted due to normal production tolerances.
- Signal path.
 - ∇ : RX (from TEL LINE)
 - \blacktriangleright : TX (to TEL LINE)
 - ∇ : bell

• IC Block Diagrams

IC101 DBL5002



IC201 HM91652B

