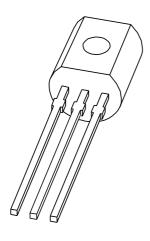
DISCRETE SEMICONDUCTORS

DATA SHEET



MPSA14 NPN Darlington transistor

Product specification Supersedes data of 1999 Apr 27

2003 Oct 22





NPN Darlington transistor

MPSA14

FEATURES

- High current (max. 500 mA)
- Low voltage (max. 30 V)
- High DC current gain (min. 10000).

APPLICATIONS

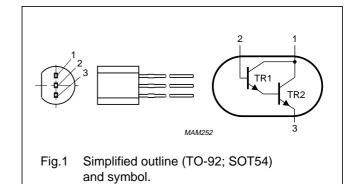
• High gain amplification.

DESCRIPTION

NPN Darlington transistor in a TO-92; SOT54 plastic package. PNP complement: MPSA64.

PINNING

PIN	DESCRIPTION			
1	collector			
2	base			
3	emitter			



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	30	V
V _{CES}	collector-emitter voltage	V _{BE} = 0	_	30	V
V_{EBO}	emitter-base voltage	open collector	_	10	V
I _C	collector current (DC)		_	500	mA
I _{CM}	peak collector current		_	1	Α
I_{B}	base current (DC)		_	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	500	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

ORDERING INFORMATION

TYPE NUMBER		PACKAGE					
I TPE NOWIBER	NAME	DESCRIPTION	VERSION				
MPSA14	_	plastic single-ended leaded (through hole) package; 3 leads	SOT54				

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Philips Semiconductors Product specification

NPN Darlington transistor

MPSA14

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	250	K/W

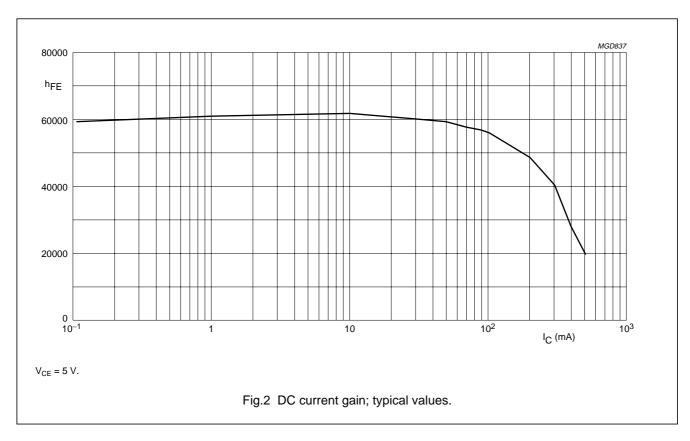
Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 30 V	_	0.1	μΑ
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 10 V	_	0.1	μΑ
h _{FE}	DC current gain	$I_C = 10 \text{ mA}$; $V_{CE} = 5 \text{ V}$; see Fig.2	10000	_	
		I _C = 100 mA; V _{CE} = 5 V; see Fig.2	20000	_	
V _{CEsat}	collector-emitter saturation voltage	I _C = 100 mA; I _B = 0.1 mA	_	1.5	٧
V _{BEsat}	base-emitter saturation voltage	I _C = 100 mA; I _B = 0.1 mA	_	1.5	٧
V _{BEon}	base-emitter on-state voltage	I _C = 100 mA; V _{CE} = 5 V	_	2	V
f _T	transition frequency	$I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}; f = 100 \text{ MHz}$	125	_	MHz



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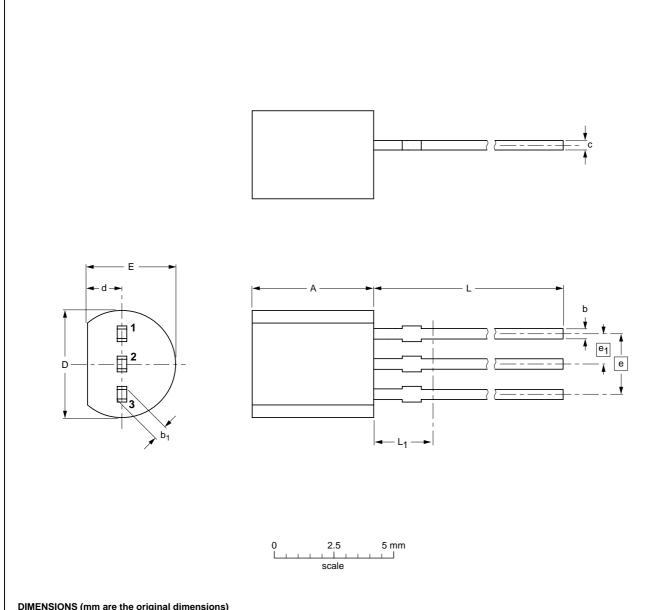
NPN Darlington transistor

MPSA14

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFERENCES			EUROPEAN ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE	
SOT54		TO-92	SC-43			97-02-28	

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NPN Darlington transistor

MPSA14

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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Notes

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- 3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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